

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Chris B. Freeman et al.
Serial No.: 10/713,784 Examiner: not yet assigned
Filing Date: November 13, 2003 Group Art Unit: 2186
Title: BUFFERED MEMORY MODULE WITH IMPLICIT TO EXPLICIT MEMORY COMMAND EXPANSION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application;
or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97 (e), or
☐ a \$180 fee under 37 CFR 1.17 (p); or
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
- ☐ under 37 CFR 1.97 (d) together with:
☐ Statement under 37 CFR 1.97 (e), and
☐ a \$180.00 fee set forth in 37 CFR 1.17 (p).

(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☒ Return Postcard

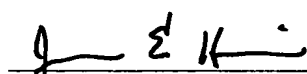
It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

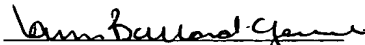
Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
Date: February 18, 2004


Lauren Ballard-Gemmell

PATENT APPLICATION

Atty. Docket No.: 5038-327
 Applicant: Chris B. Freeman et al.
 Filing Date: November 13, 2003

Serial No.: 10/713,784
 Group: 2186



INFORMATION DISCLOSURE CITATION
 FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
_____	_____	6,185,644	Feb. 6, 2001	Farmwald et al		
_____	_____	6,317,352	Nov. 13, 2001	Halbert et al.		
_____	_____	6,345,321	Feb. 5, 2002	Litaize et al.		
_____	_____	6,369,605	Apr. 9, 2002	Bonella et al.		
_____	_____	6,493,250	Dec.10, 2002	Halbert et al.		
_____	_____	6,502,161	Dec. 31, 2002	Perego et al.		
_____	_____	6,625,687	Sep. 23, 2003	Halbert et al		
_____	_____	6,643,752	Nov. 4, 2003	Donnelly et al.		

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name
_____	_____	WO 99/30240	17 Jun. 1999	WIPO	Holman
_____	_____	WO 99/41667	19 Aug. 1999	WIPO	Holman
_____	_____	WO 99/41666	19 Aug. 1999	WIPO	Holman

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.
_____	_____	SBS Implementers Forum; <i>System Management Bus Specification</i> ; August 3, 2000; Pages 1-59
_____	_____	Atmel Corporation; <i>2-Wire Serial EEPROM Revision 0180D</i> ; October 1998; Pages 1-21;
_____	_____	Intel Corporation; <i>PC SDRAM Serial Presence Detect Specification Revision 1.2B</i> ; November, 1999; Pages 1-30

Examiner: _____

Date Considered: _____